

Parameterised Systems

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Parameterised System

Motivations: analysis of a whole family of systems $(\mathcal{S}_p)_p$.

$p \in Param$ is a parameter.

- **Synthesis:** “Does there exists $p \in Param$ such that $\mathcal{S}_p \models \varphi$?”
- **Validity:** “Does $\mathcal{S}_p \models \varphi$ for all p ?”

Parameterised System

$$\underline{E \leq C}$$

Different types of parameters:

- Guard constants values (in timed automata):

$$Param \subseteq \mathbb{R}^+;$$

- Probability values (in stochastic systems):

$$Param \subseteq [0, 1];$$

$$\underline{P}$$

- ...

- Number of interacting copies of processes:

$$Param = \mathbb{N}.$$

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Today: **Validity** of parameterised systems composed with **arbitrary many copies** using **well-quasi order** techniques.

Arbitrary many copies



Arbitrary many copies

Definition

A parameterised system $\mathcal{S} = (S, I, \Sigma, \rightarrow)$ is a LTS (S, Σ, \rightarrow) equipped with

- A WQO \leq over S ;
- An upward-closed set of initial states I ;
- A norm function $|\cdot| : S \rightarrow \mathbb{N}$ such that $\forall (s, t), s \leq t \Rightarrow |s| < |t|$

We write $S_k = \{s \in S \mid |s| = k\}$ and \mathcal{S}_k the LTS restricted to initial states in $I \cap S_k$.

$S \succcurlyeq \mathcal{S}_k$



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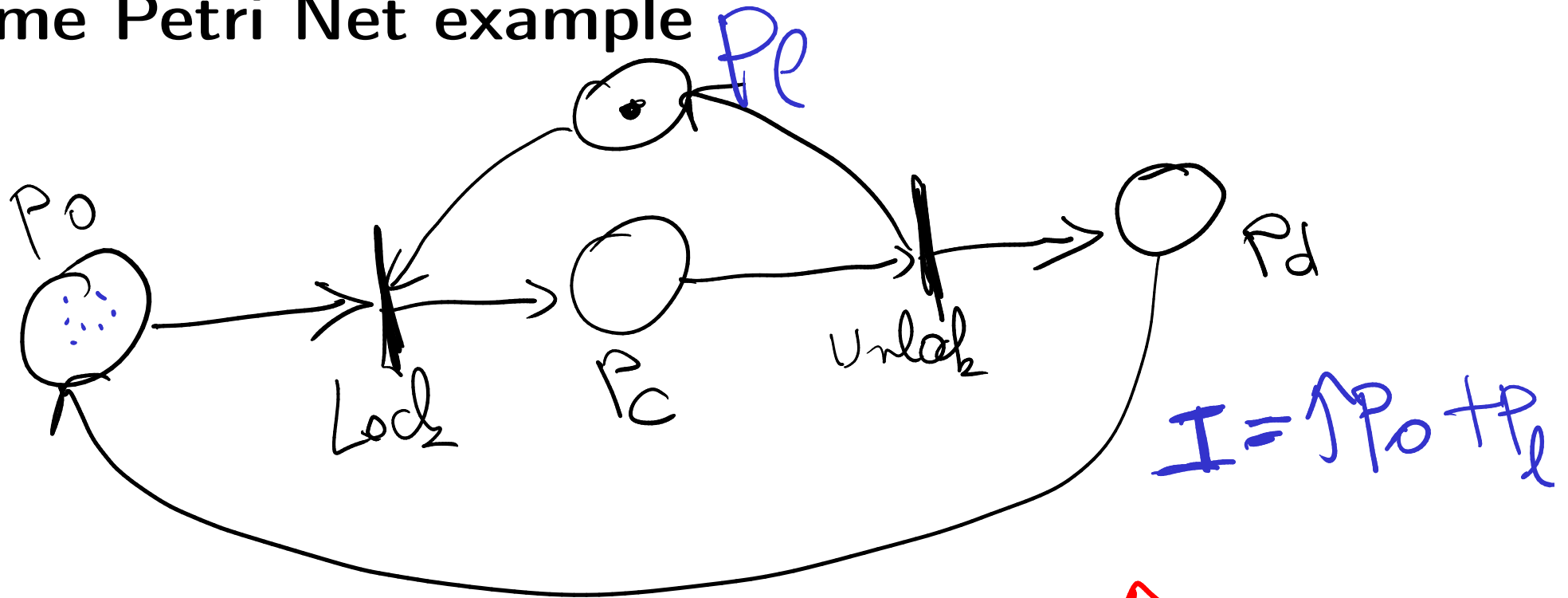
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Not necessarily a WSTS!



Some Petri Net example



$$Bad = 12p_c$$

$$|M| = m(p_0) + m(p_c) + m(p_d)$$

$$I = \{k \cdot p_0 \mid k \in \mathbb{N}\},$$

$$|M| = \dots,$$

$$M \leq M' \text{ if } \forall p \in P, M(p) \leq M'(p)$$

Some Petri Net example

$I = \{k \cdot p_0 \mid k \in \mathbb{N}\},$ \leftarrow w.p.w. closed

$|M| = \dots,$

$M \leq M'$ if $\forall p \in P, M(p) \leq M'(p)$ **and**

$\text{support}(M) = \{p \mid M(p) > 0\} = \{p \mid M'(p) > 0\} = \text{support}(M').$

(still a WQO)

Remark:

$|M|$ is not necessarily the number of tokens in M .

Parameterised Systems (Our definition today)

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- A norm function $|\cdot| : S \rightarrow \mathbb{N}$ such that $\forall (s, t), s \leq t \Rightarrow |s| \leq |t|$.
- \rightarrow **preserves the norm**
- S_k **is finite for any k .**

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Cut-off property

Definition

A parameterised system $(\mathcal{S}_n)_n$ has a **cut-off property** for φ if there exists a **cut-off bound** $n \in \mathbb{N}$, such that

$$\forall k > n, \underbrace{\mathcal{S}_k \models \varphi} \Leftrightarrow \underbrace{\mathcal{S}_n \models \varphi}$$

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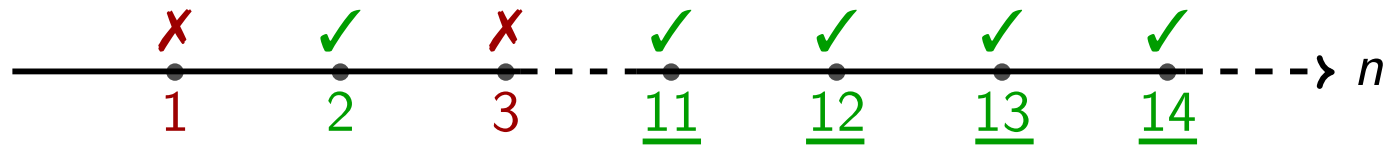
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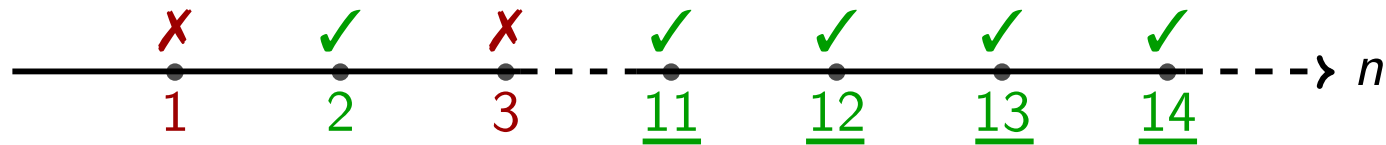
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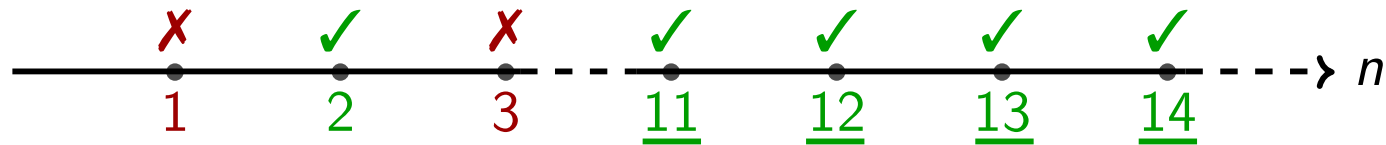
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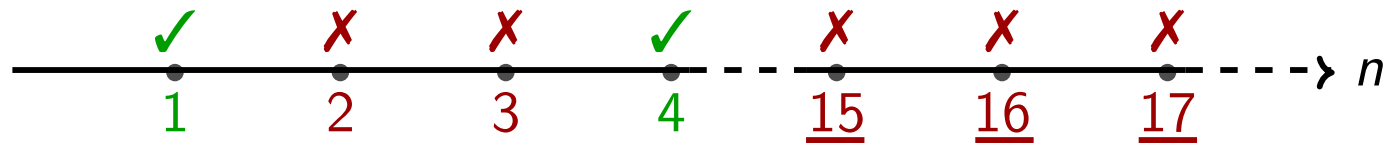
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- (Recall week 3): for any M , if there is M' in $KarpMillerTree(N)$ with $M \leq M'$, then N can cover M .

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- **Karp-Miller tree** from $M_0 = \omega \cdot p_0$.
- (Recall week 3): for any M , if there is M' in $KarpMillerTree(N)$ with $M \leq M'$, then N can cover M .
- **Conclusion:** the problem is EXPSPACE-complete.

Recall: Backward coverability for WSTS

function BackCoverFormal($\mathbf{N}, p_0, \text{Bad}, \leq$)

$U \leftarrow \text{Bad}$

$U' \leftarrow \emptyset$

while $U \neq U'$ do

$U' \leftarrow U$

$U \leftarrow U \cup \text{Pre}(U)$

return $\exists k : k \cdot p_0 \in U$

// U stays up-closed

Non-atomic operations on shared variables

Inspired by:

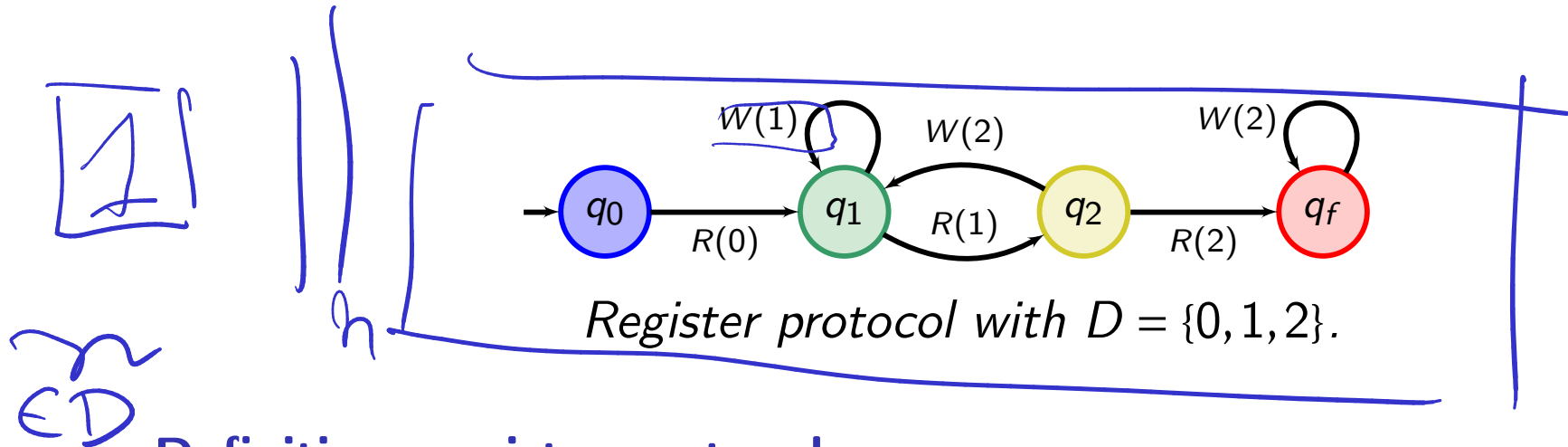
- *Model checking parameterized asynchronous shared-memory systems.*

Antoine Durand-Gasselin, Javier Esparza, Pierre Ganty, Rupak Majumdar, 2017

- *Reachability in Networks of Register Protocols under Stochastic Schedulers*

Patricia Bouyer, Nicolas Markey, Mickael Randour, Arnaud Sangnier, Daniel Stan, 2016

The model

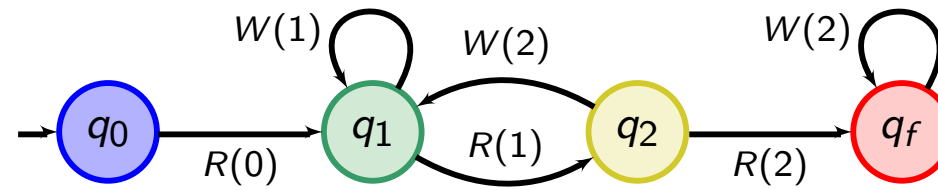


Definition: register protocol

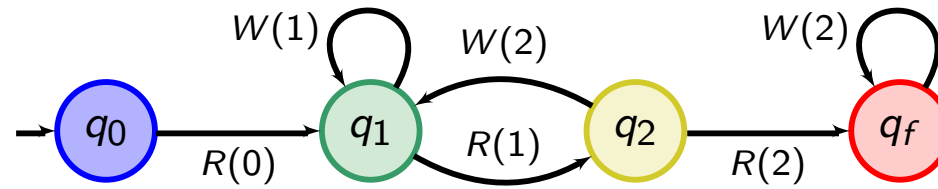
$$\mathcal{P} = \langle Q, D, q_0, d_0, q_f, T \rangle$$

- $\langle Q, q_0, q_f, T \rangle$ is a finite state automaton;
- D finite dataset for the shared register;
- d_0 an initial value;
- $T \subseteq Q \times \{R, W\} \times D \times Q$ set of transitions, labelled by read and write operations over D .

The model



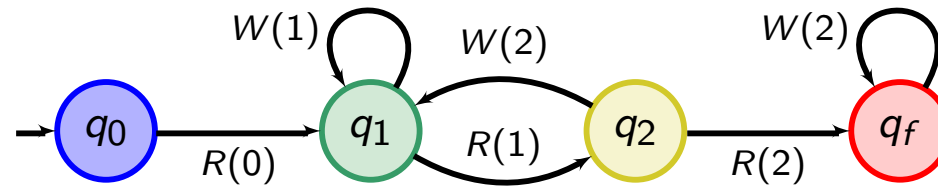
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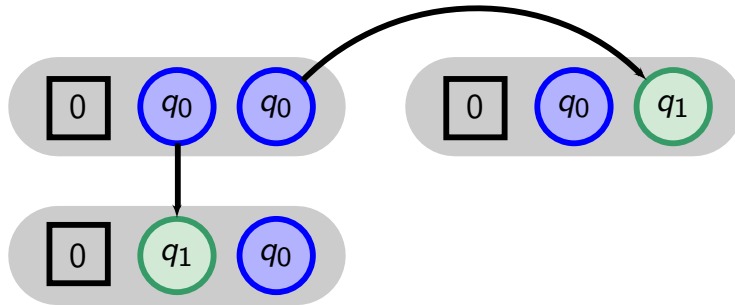
Network for two processes (self-loops omitted).



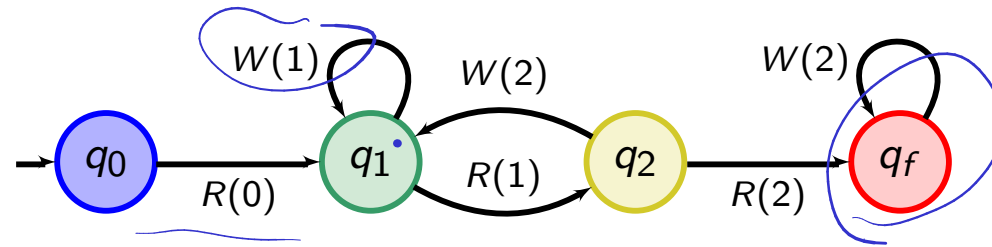
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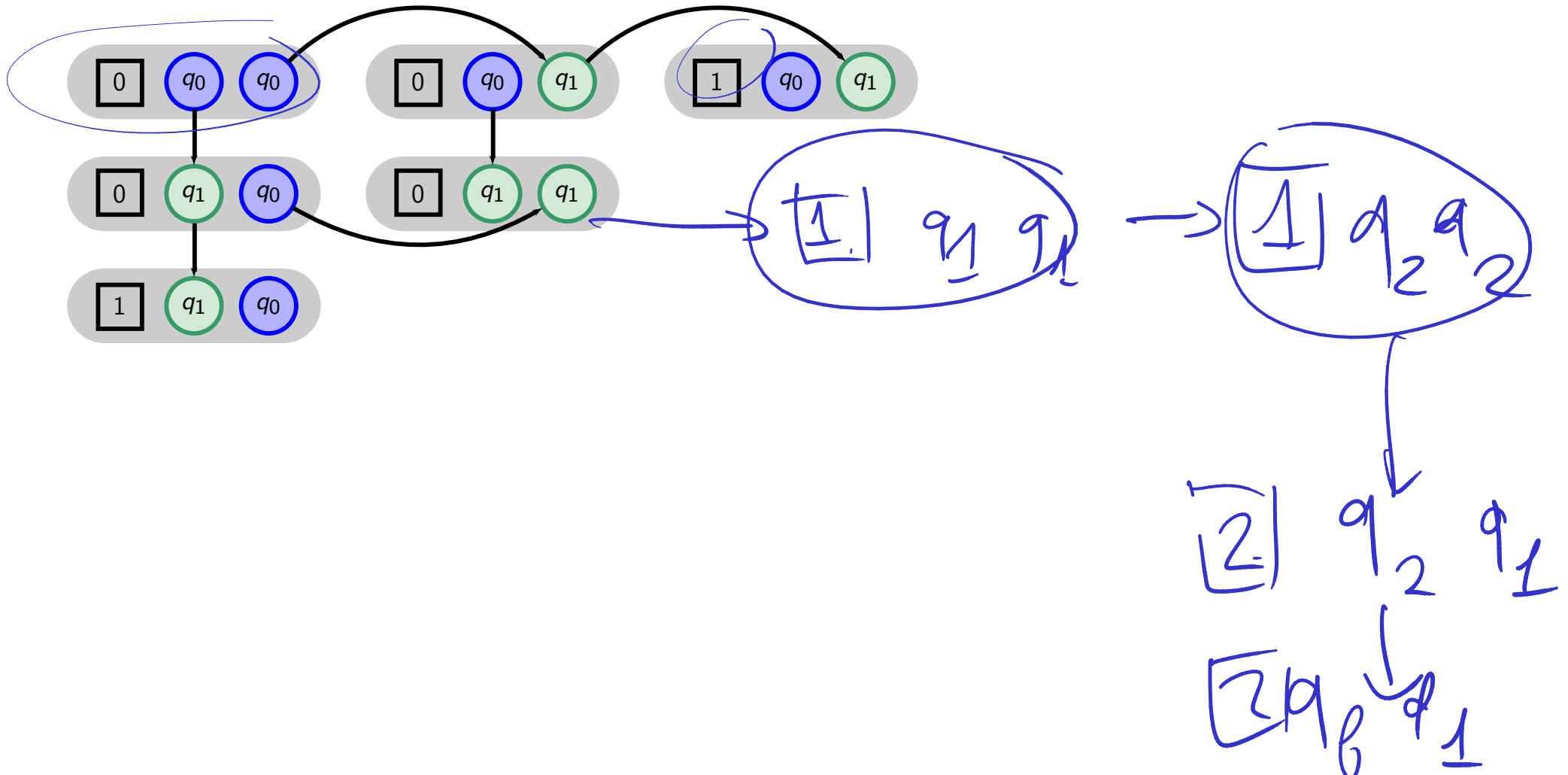
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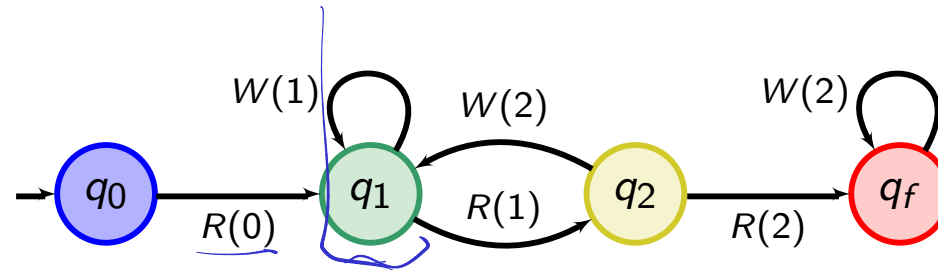
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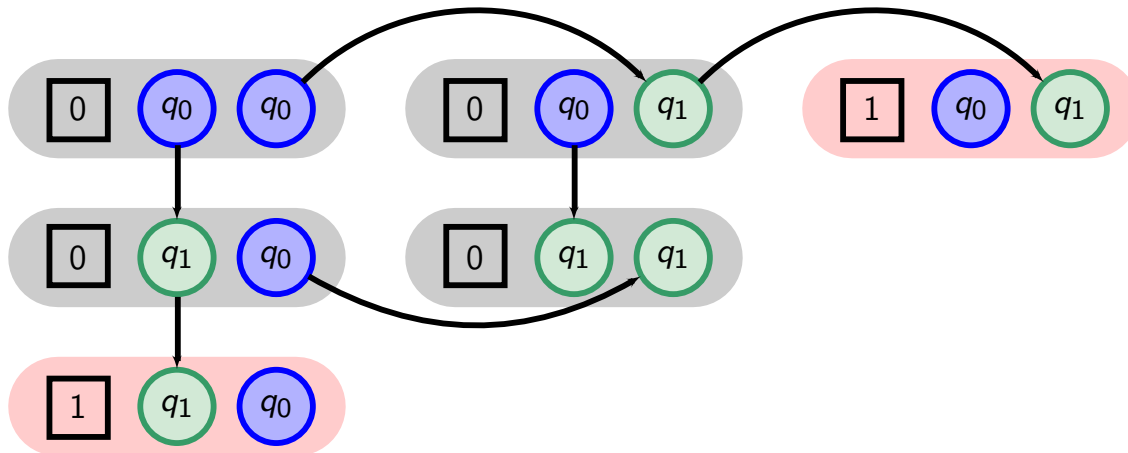
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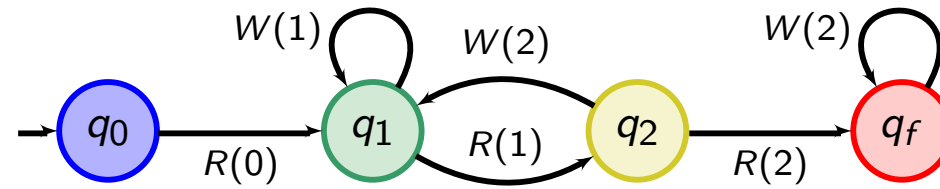


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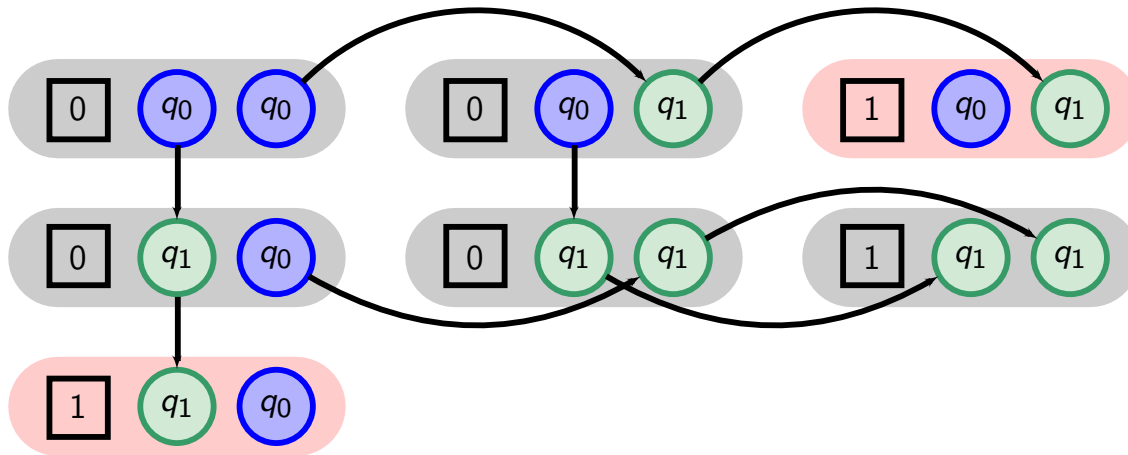


- There exist paths from there, the processes in q_0 are trapped;

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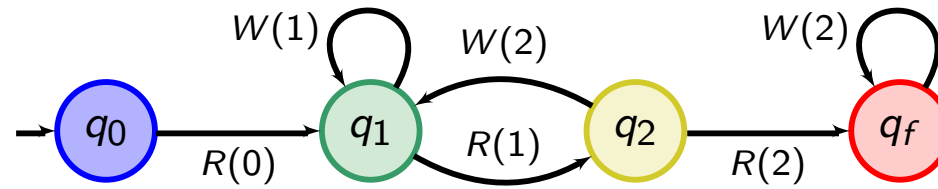
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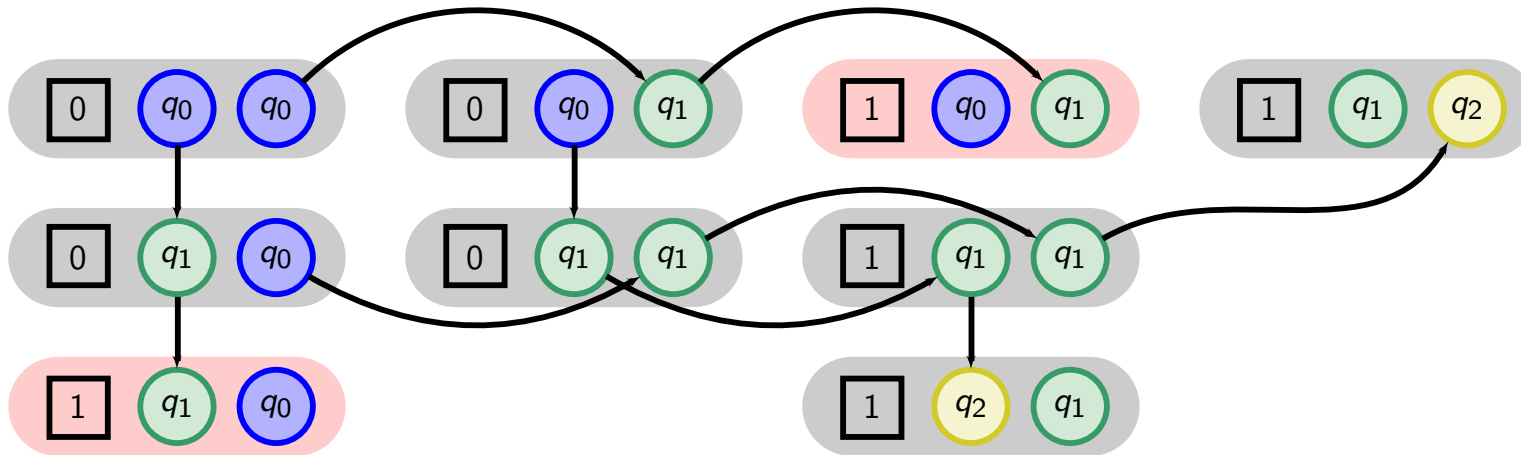
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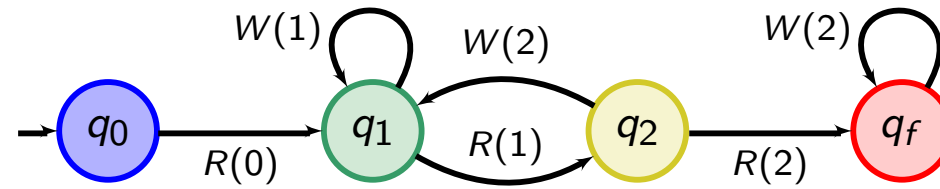
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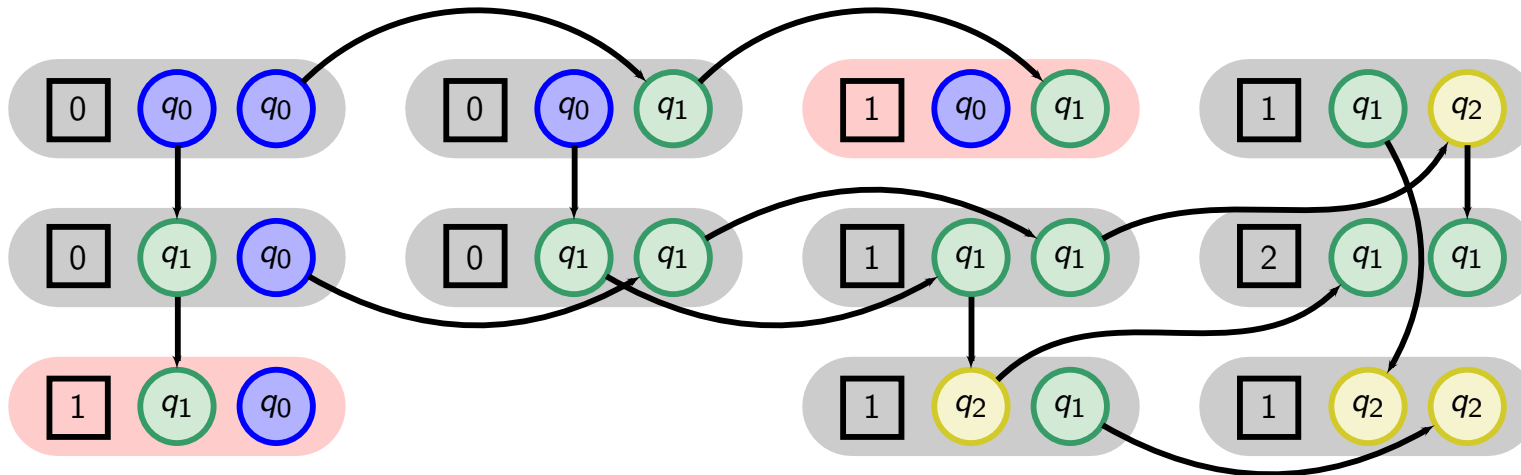
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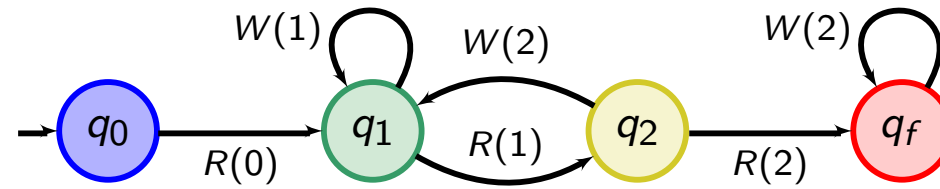
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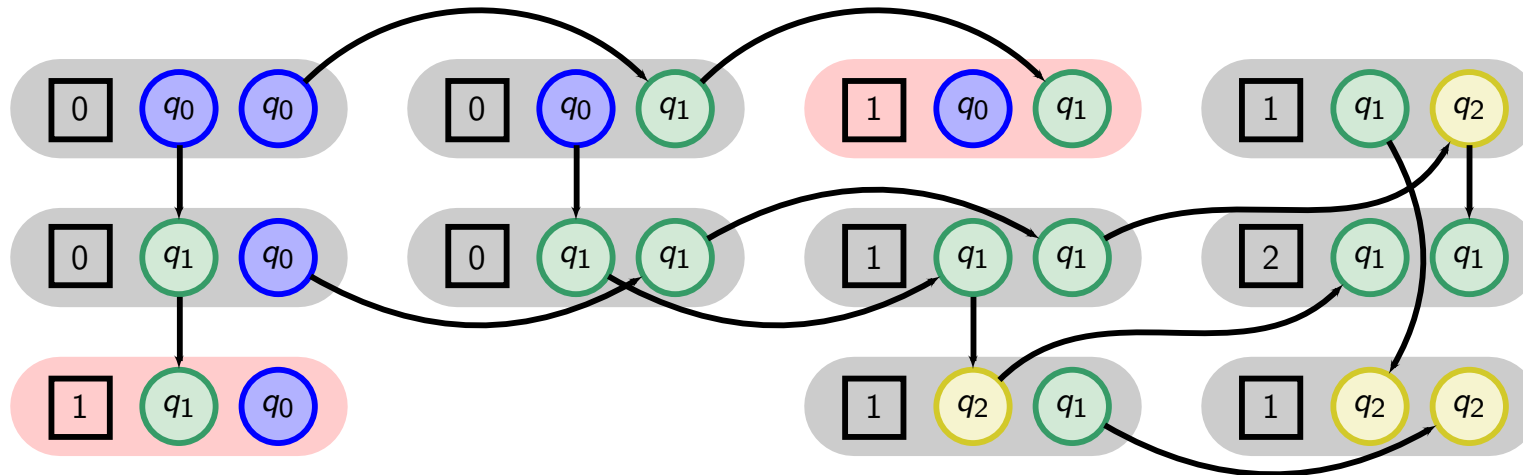
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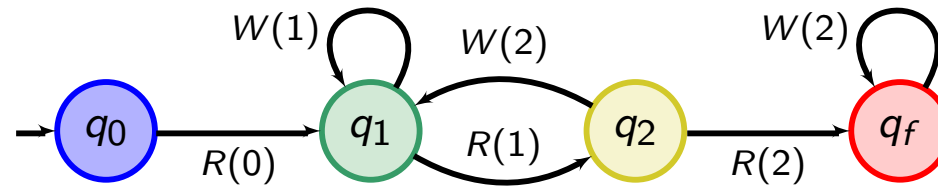
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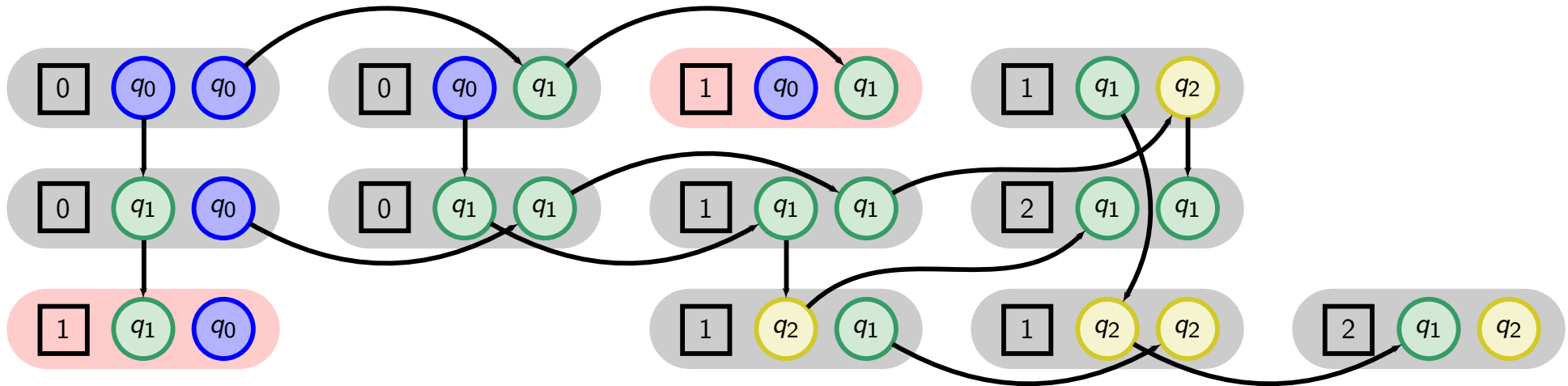
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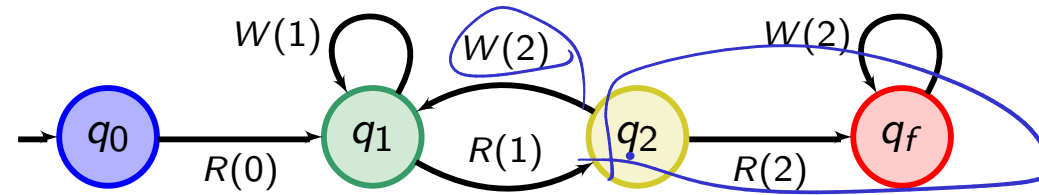
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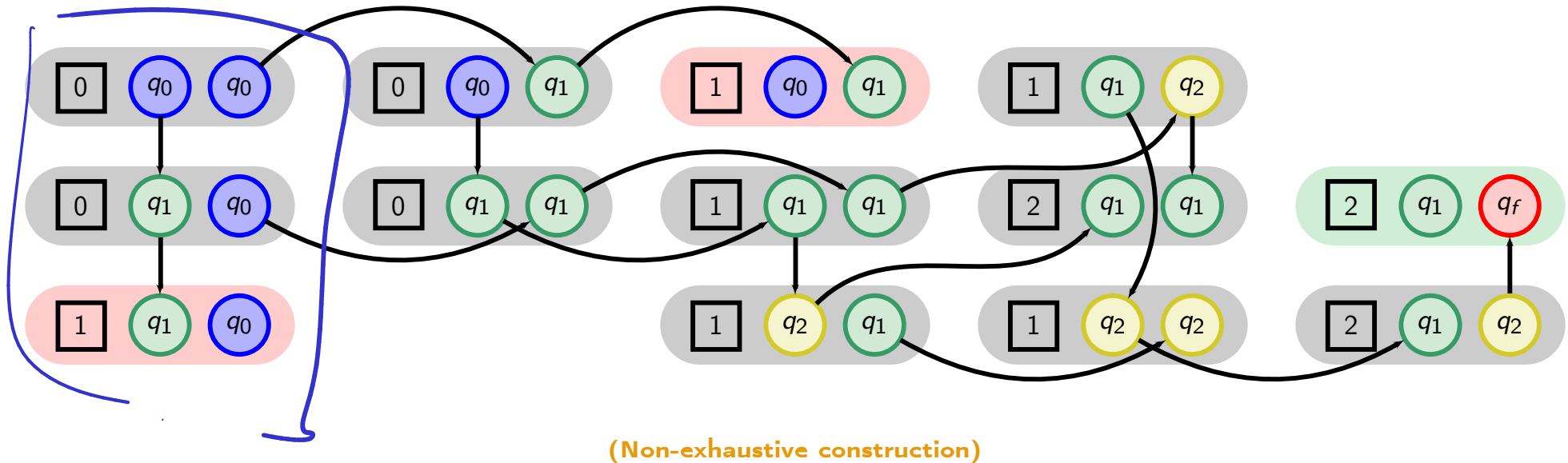
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Network for two processes (self-loops omitted).



- There exist paths from there, the processes in q_0 are trapped;
- There exist paths that reach q_f ...
- ...and they require at least two processes.

- A **configuration** $s \in S$ is a multiset of states ^{control} + a register value
 $d \in D$; 

- A **configuration** $s \in S$ is a multiset of states + a register value

$d \in D$; 

- **Parameter**: once the system is started, the configuration has a **fixed** size;
- **Interleaving** semantics;
- **Non-atomic** operations (read or write at a time);
- Goal: reach a configuration which **covers** q_f .

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- **Semantics**:

- How does the **scheduler** work ?

Non-deterministic Scheduler Case: Reachability/Safety

- The scheduler is **helpful**;
- **Monotonicity**: if q_f is reachable with n initial processes, it is with $n + 1$;

—————→ n

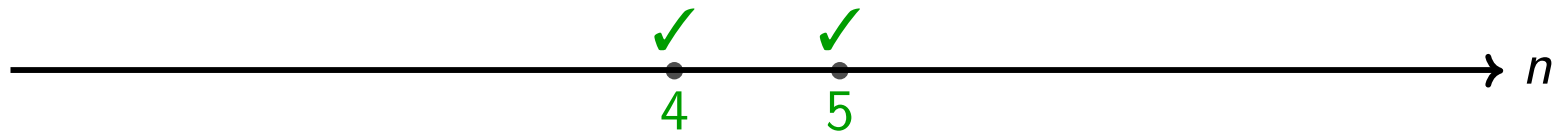
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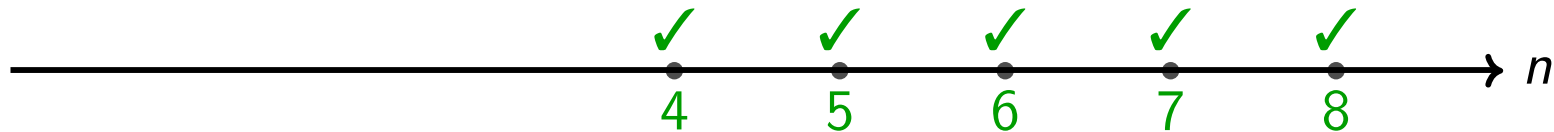
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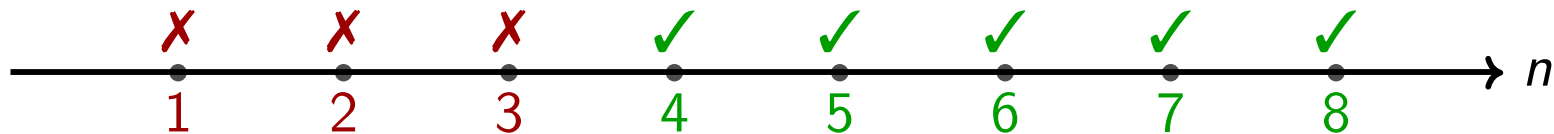
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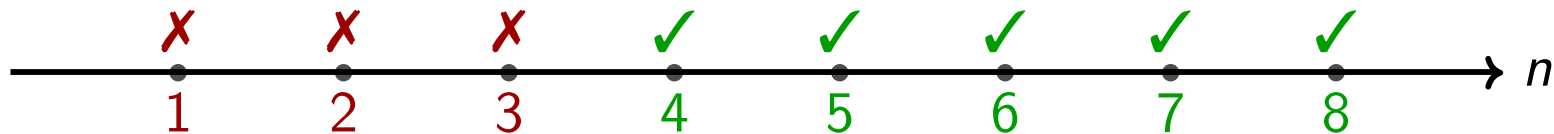


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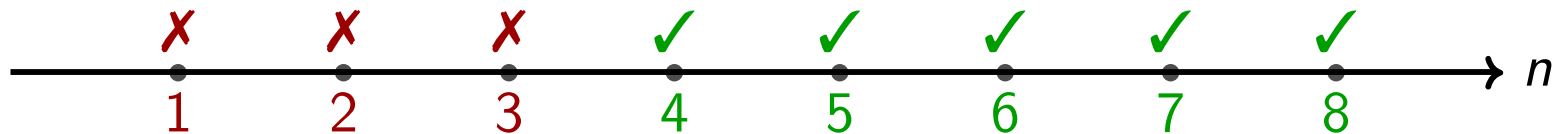
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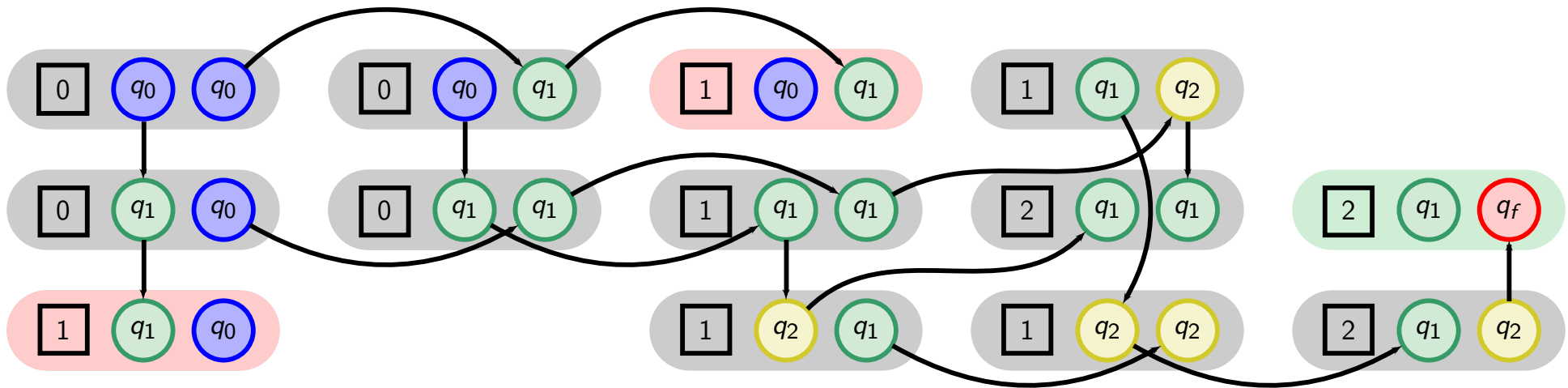
Proof: check last appearance and dis-appearance of each control state...
Gives a bound on the runtime of the backward coverability algorithm.

Fair, Probabilistic scheduler

- We don't control the scheduler anymore;
- **Stochastic** behaviour (environment);
- Finite patterns cannot be repeated infinitely often;
- We consider **almost-sure** reachability: $\mathbb{P}_n(\diamond \uparrow q_f) \stackrel{?}{=} 1$

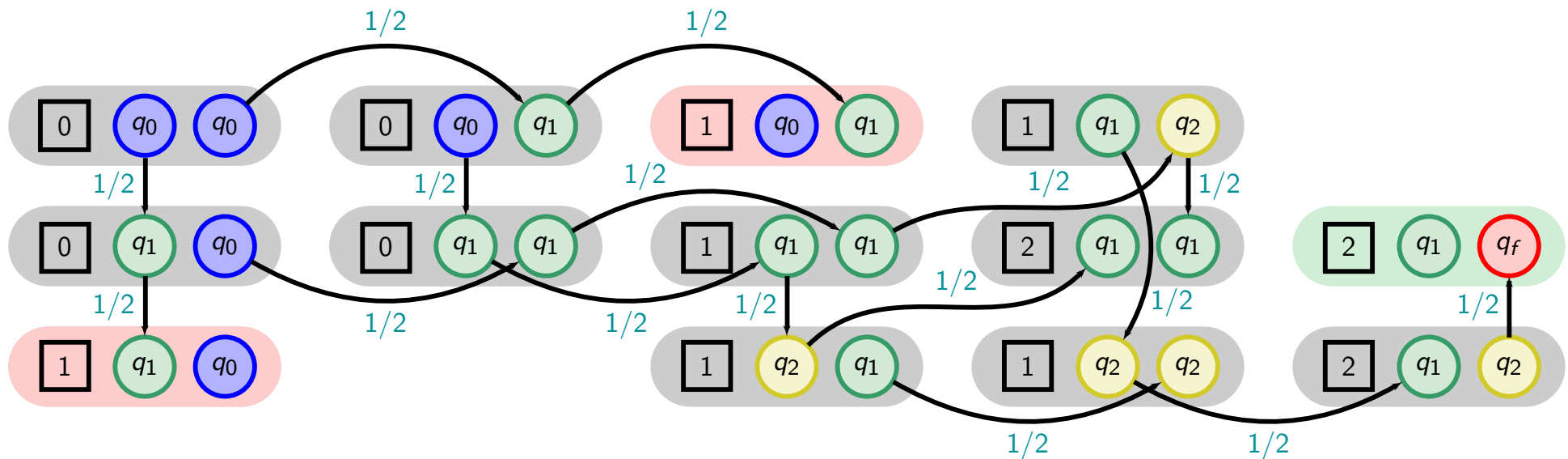
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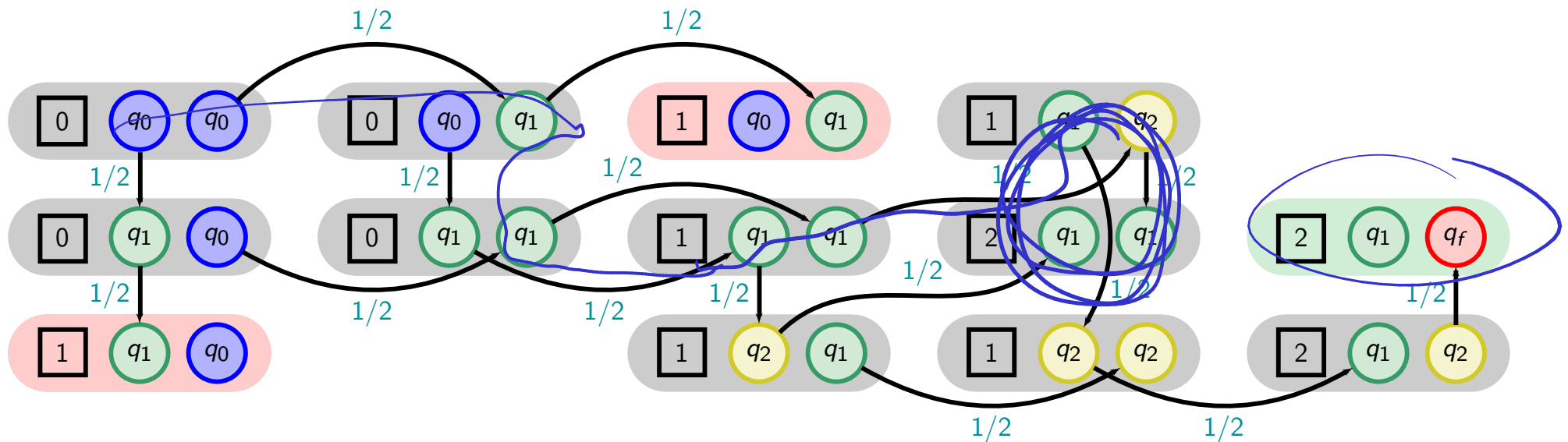
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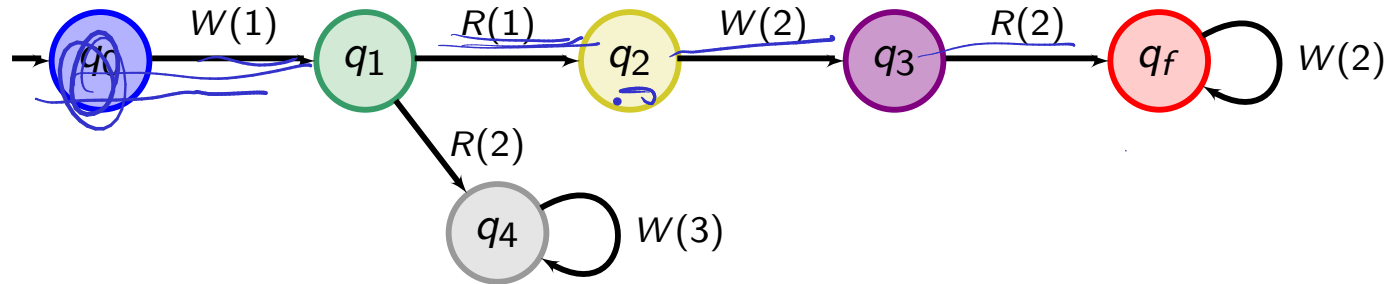


Qualitative property
+
Finite configuration space

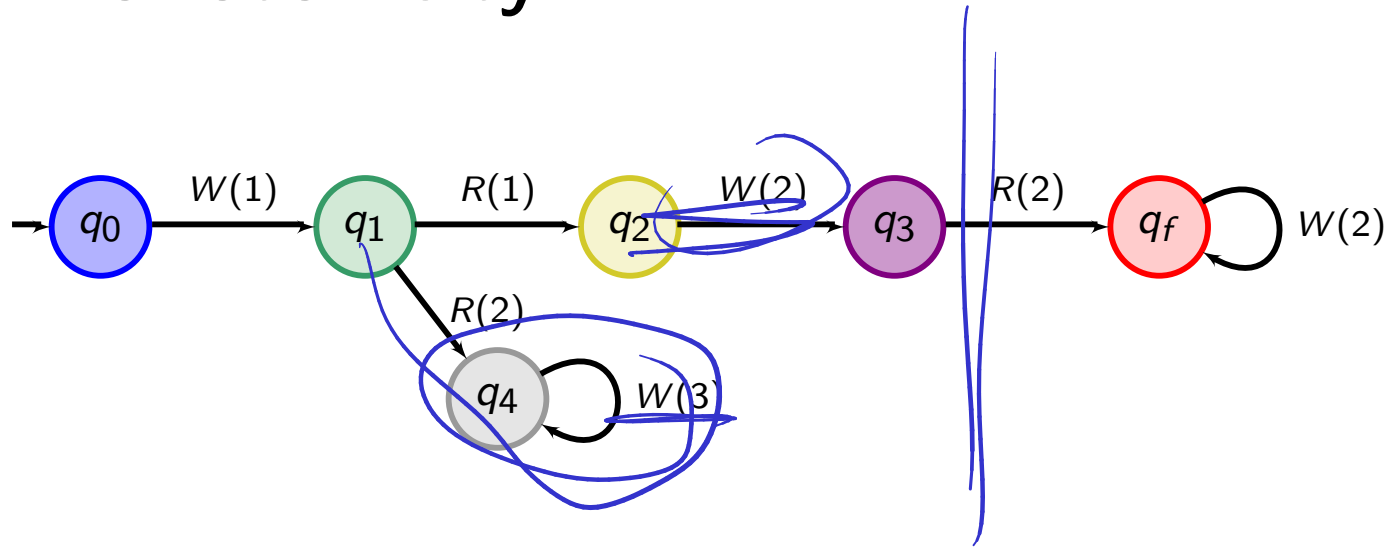


Exact probability values are
not relevant.

Lack of monotonicity

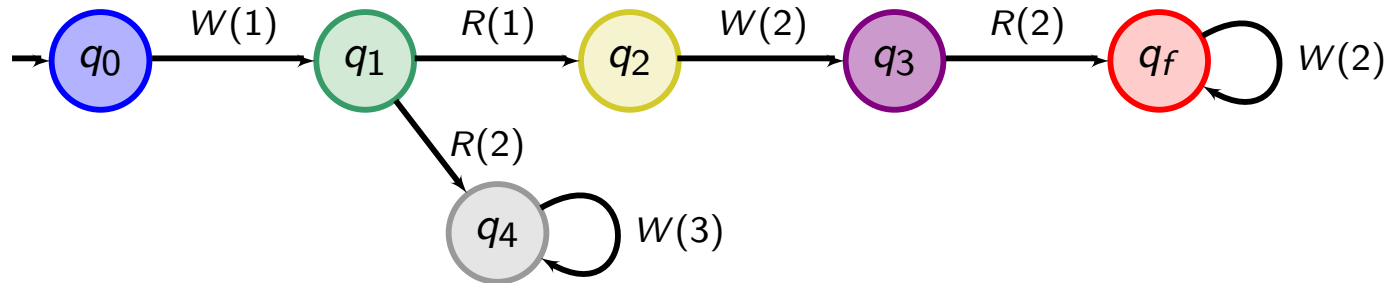


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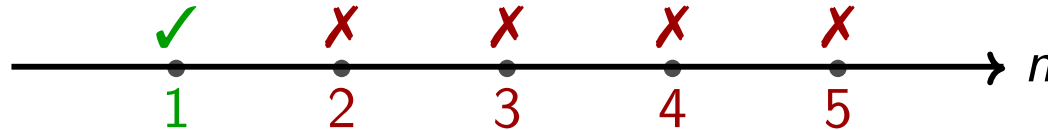


⇒ Additional processes can create new deadlocks!

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Proof (sketch): If $\mathbb{P}_k(\diamond \uparrow q_f) = 1$, then any reachable state $s \in S_k$ is reached with positive probability so should still reach $\uparrow q_f$ with probability 1, in particular $s \in Pre^*(\uparrow q_f)$. Let's prove the reverse implication:

Assume $Post^*(I \cap S_k) \subseteq Pre^*(\uparrow q_f)$, then:

- For any reachable state s , there exists a path to reach $\uparrow q_f$ so the probability for this to happen is some positive number $f(s) > 0$.

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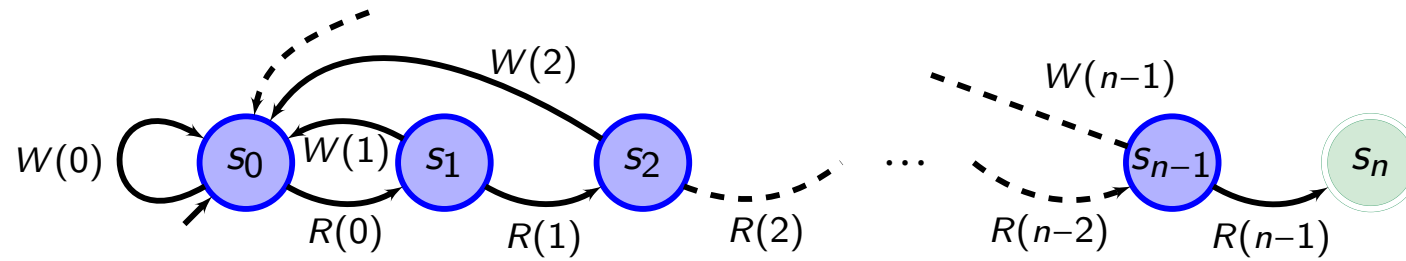
$$\text{Post}^*(I \cap S_{=k}) \subseteq \text{Pre}^*(\uparrow q_f)$$

Proof (sketch): If $\mathbb{P}_k(\diamond \uparrow q_f) = 1$, then any reachable state $s \in S_k$ is reached with positive probability so should still reach $\uparrow q_f$ with probability 1, in particular $s \in \text{Pre}^*(\uparrow q_f)$. Let's prove the reverse implication:

Assume $\text{Post}^*(I \cap S_{=k}) \subseteq \text{Pre}^*(\uparrow q_f)$, then:

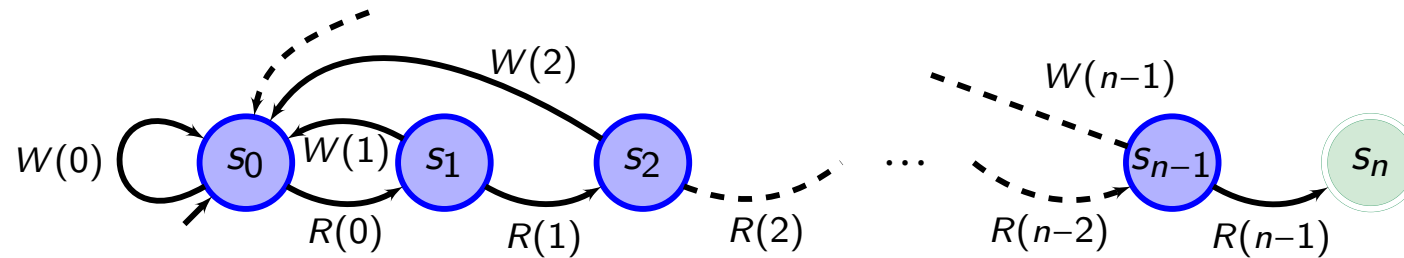
- For any reachable state s , there exists a path to reach $\uparrow q_f$ so the probability for this to happen is some positive number $f(s) > 0$.
- At any time point, the probability of eventually reaching $\uparrow q_f$ is at least $\max_{s \in S_k} f(s)$ which is **positive** since S_k is **finite**.

Examples



"Filter" protocol \mathcal{F}_n for $n > 0$.

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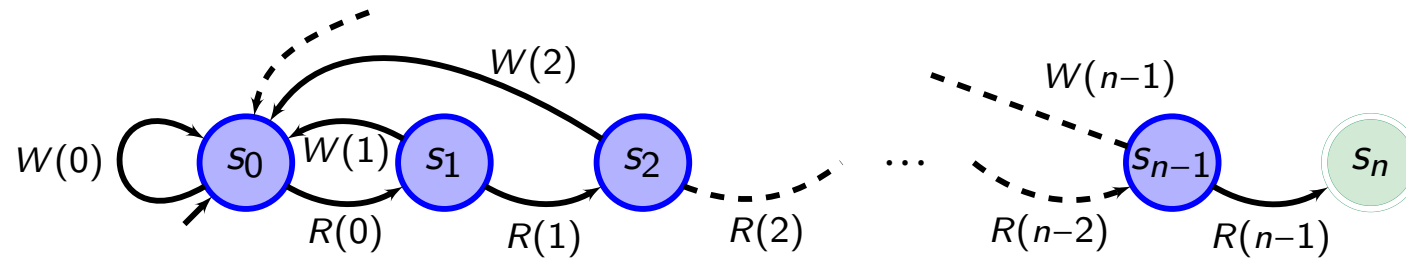
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For protocol \mathcal{F}_n ,

- ▷ networks of size $\geq n$ cover s_n with probability 1,
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\Rightarrow **Tight positive cut-off equal to n , i.e., linear in the protocol size.**

Existence of a cut-off

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For any register protocol \mathcal{P} **there always exists a cut-off for almost-sure reachability**

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⚠ This result strongly relies on the fact that **both** $(S, \preceq, \rightarrow)$ and $(S, \preceq, \rightarrow^{-1})$ are **WSTS**

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⚠ This result strongly relies on the fact that **both** $(S, \preceq, \rightarrow)$ and $(S, \preceq, \rightarrow^{-1})$ **are WSTS**

The non-atomicity guarantees that when a process takes a transition, all processes in the same state can also take the same transition (with a non-zero probability).

\Rightarrow **a.k.a. copycat lemma.**

Existence: quick sketch (1/2)

- Write $\text{Pre}^*(\uparrow q_f) \subseteq S$ the set of configurations that can reach q_f ;
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$$\exists n \text{ Post}^*(I \cap S_{\geq n}) \subseteq \text{Pre}^*(\uparrow q_f)$$

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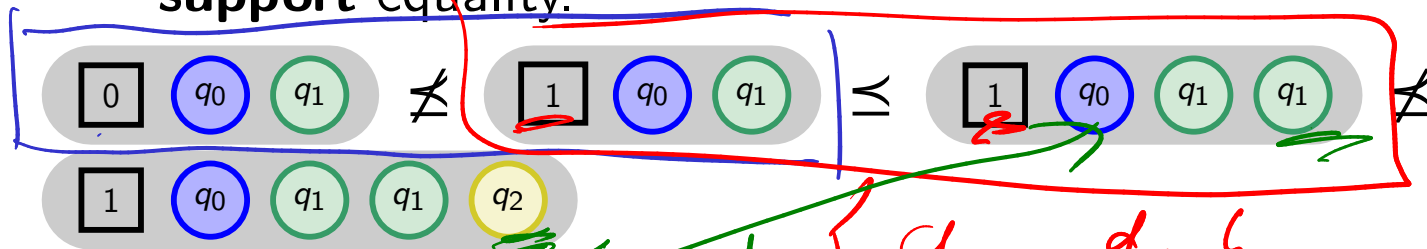
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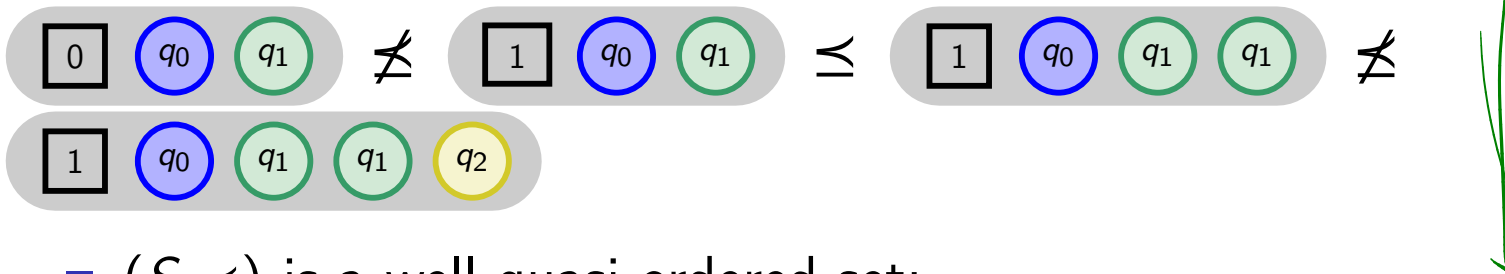
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- Consider \preceq point-wise order over configurations, **with state-value support** equality.



- (S, \preceq) is a well-quasi-ordered set;
- Pre^* and $\text{Post}^*(S_{\geq n})$ are upward-closed for any n ;

Existence: quick sketch (2/2)

1 $\text{Post}^*(\underline{I \cap S_{\geq 1}}) = \uparrow \{\theta_1, \dots, \theta_l\}$ and $\underline{\text{Pre}^*} = \uparrow \{\eta_1, \dots, \eta_m\}$.

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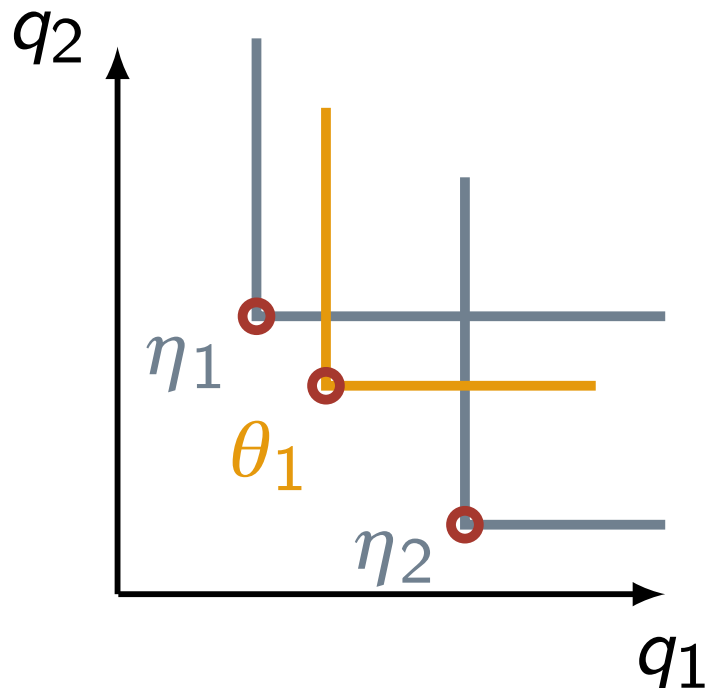
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... intuitively, the goal is to check if elements of $\text{Post}^*(I \cap S_{\geq n})$ can enter Pre^* by adding sufficiently many processes in a given state.

Lemma 1. *Let $(S, I, \leq, |\cdot|)$ a parameterised system such that*

1. $(S, \leq, \rightarrow^{-1})$ is a WSTS;
2. *For any $s, s' \in S$ with $s \leq s'$ and for any $k \in [|s|, |s'|]$, there exists t of size k such that $s \leq t \leq s'$.*

Then for any upward closed set U , there exists N such that, either

$$\forall k \geq N, \text{Post}^*(I \cap S_{=k}) \subseteq U$$

either,

$$\forall k \geq N, \text{Post}^*(I \cap S_{=k}) \not\subseteq U$$

Proof. Let $K = \{k \mid \text{Post}^*(I \cap S_{=k}) \not\subseteq U\}$.

- If K is finite, take $N = 1 + \max K$. Then $\forall k \geq N, \text{Post}^*(I \cap S_{=k}) \subseteq U$.
- If K is infinite, for any $k \in K$, let $i_k \in I \cap S_{=k}$ and $s_k \in \text{Post}^*(i_k) \setminus U$. Since (S, \leq) is a WQO, we can extract an infinite subset $K' \subset K$ such that for all $k_1, k_2 \in K'$ with $k_1 \leq k_2$, $i_{k_1} \leq i_{k_2}$ but also (by extracting another subsequence), $x_{k_1} \leq x_{k_2}$. We take $n = \min K$, for any $k \geq N$, since K' is infinite, there exists $k' \in K'$ such that $n \leq k \leq k'$. Since $x_n \leq x_{k'}$ there exists x_k such that $x_n \leq x_k \leq x_{k'}$ with $|x_k| = k$. Since $i_n \rightarrow^* x_n \leq x_k$ and $(S, \leq, \rightarrow^{-1})$ is a WQO, there exists i_k such that $i_n \leq i_k \rightarrow^* x_k$ hence $x_k \in \text{Post}^*(I \cap S_{=k})$. Moreover, \overline{U} is downward closed so $x_k \notin U$.

□